

Electronic Patent Application Fee Transmittal

Application Number:	10574030			
Filing Date:	27-Mar-2006			
Title of Invention:	Two-transistor memory cell and method for manufacturing			
First Named Inventor/Applicant Name:	Robertus Theodorus Van Schaijk			
Filer:	Robert J. Crawford/Kelly Waltigney			
Attorney Docket Number:	NL03 1167 US1			
Filed as Large Entity				
U.S. National Stage under 35 USC 371 Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
Total in USD (\$)				810